

pixel 30

display frame 10

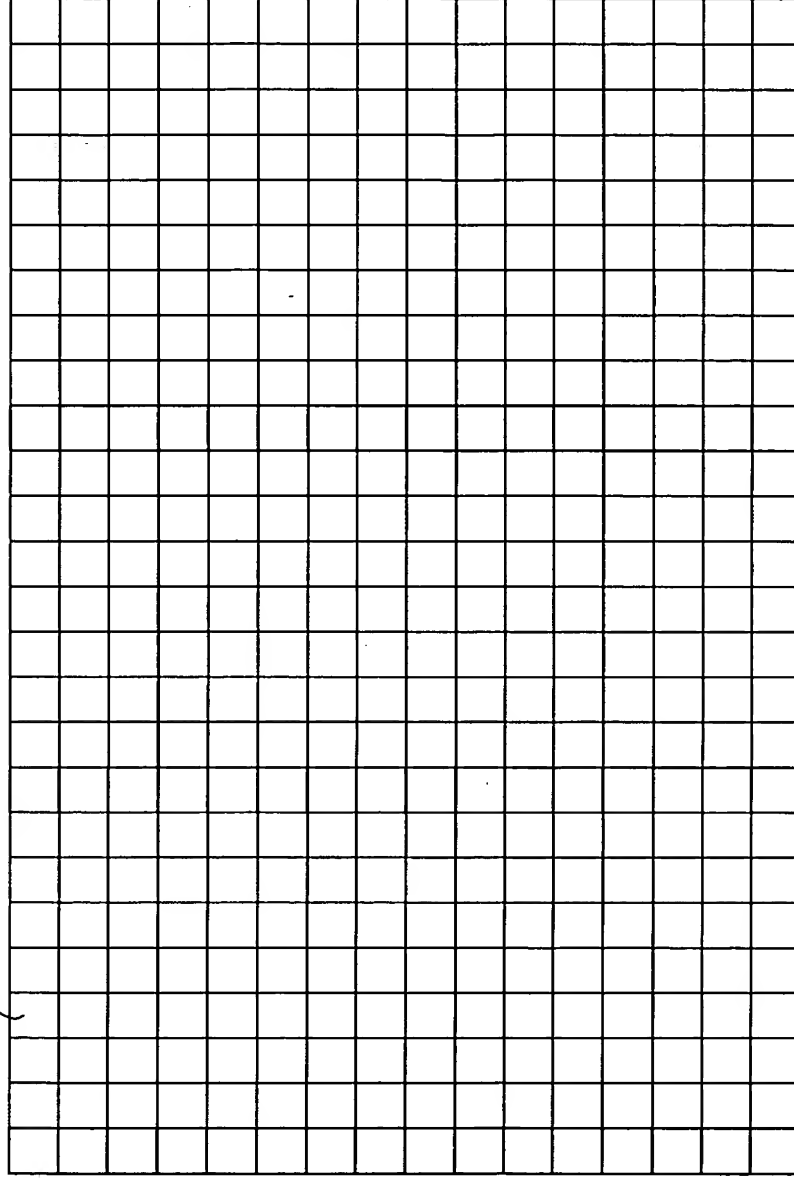


FIG. 1

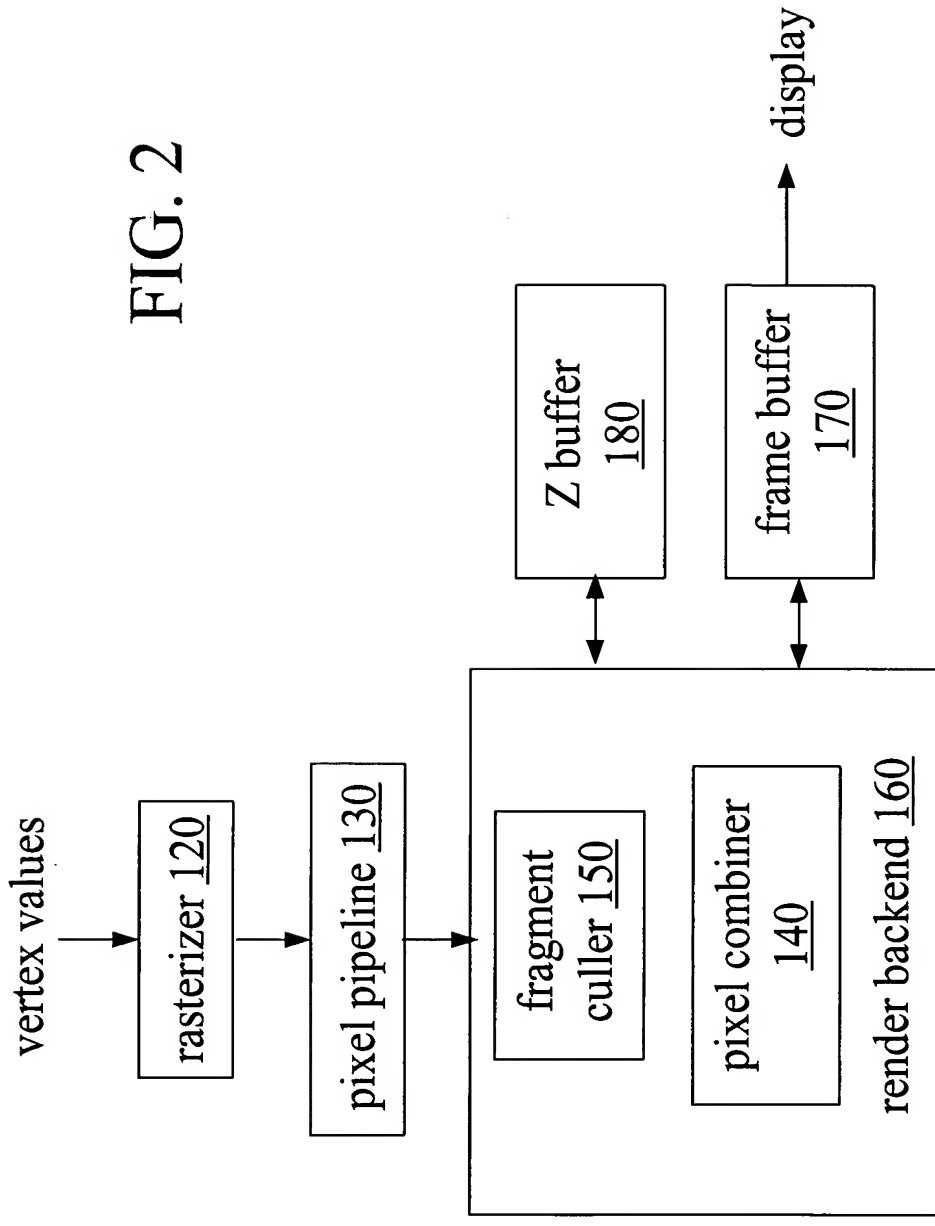


FIG. 2

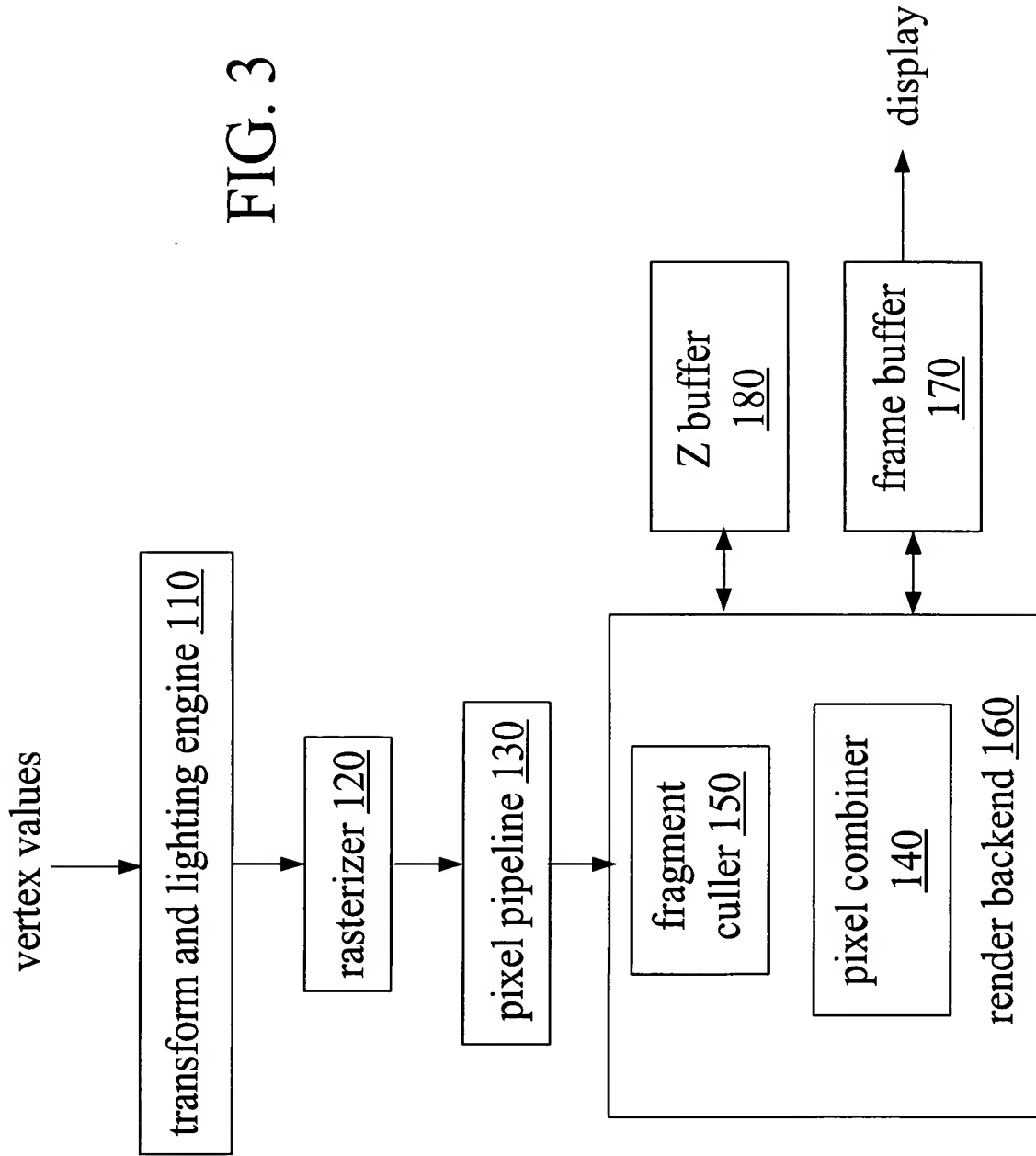


FIG. 3

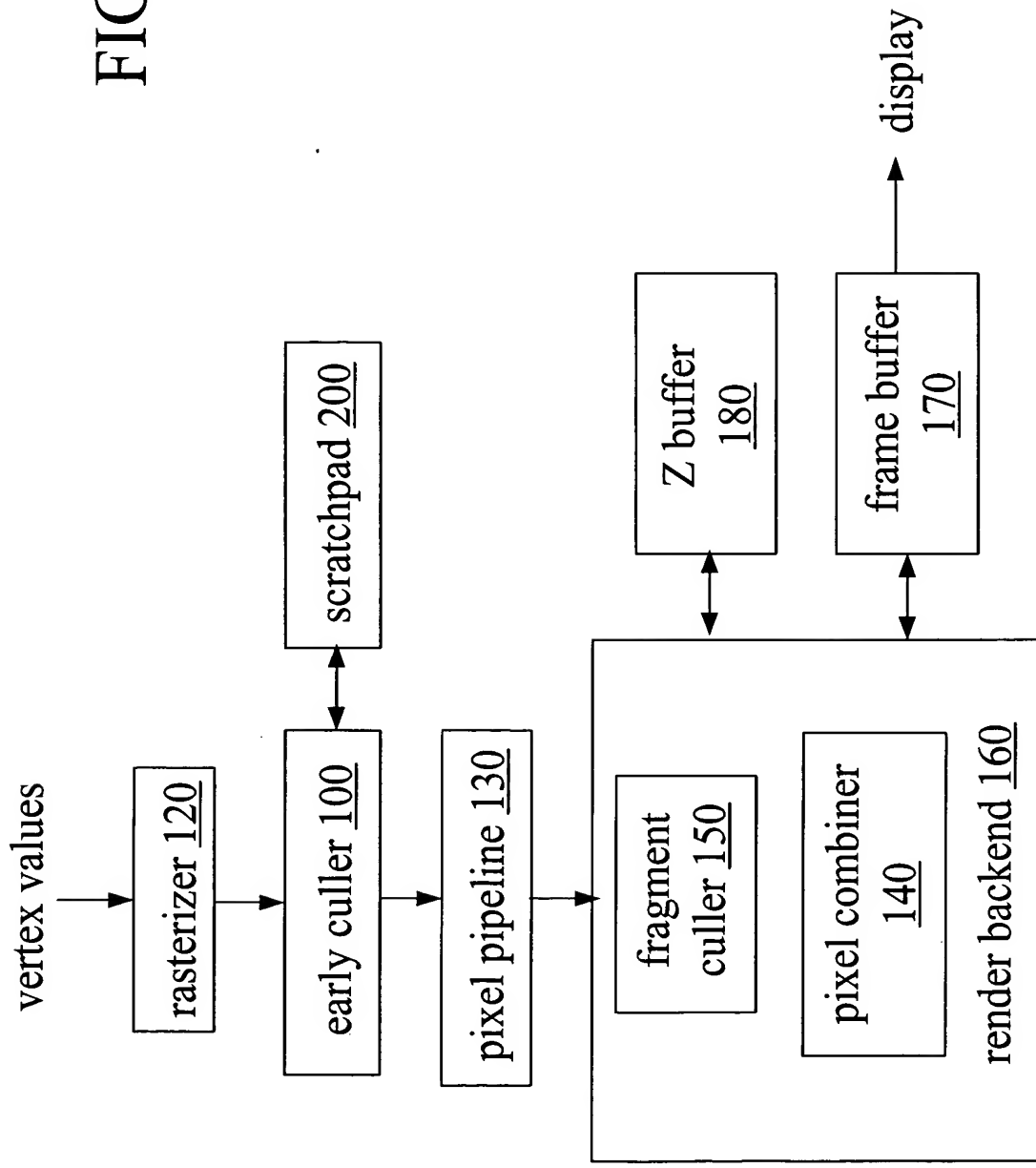


FIG. 4

FIG. 5 is a schematic diagram of a display frame 10, showing a grid of pixels. A portion of the grid is magnified to show a pixel block 20, which is a 4x4 grid of pixels. One pixel within the block is labeled pixel 30.

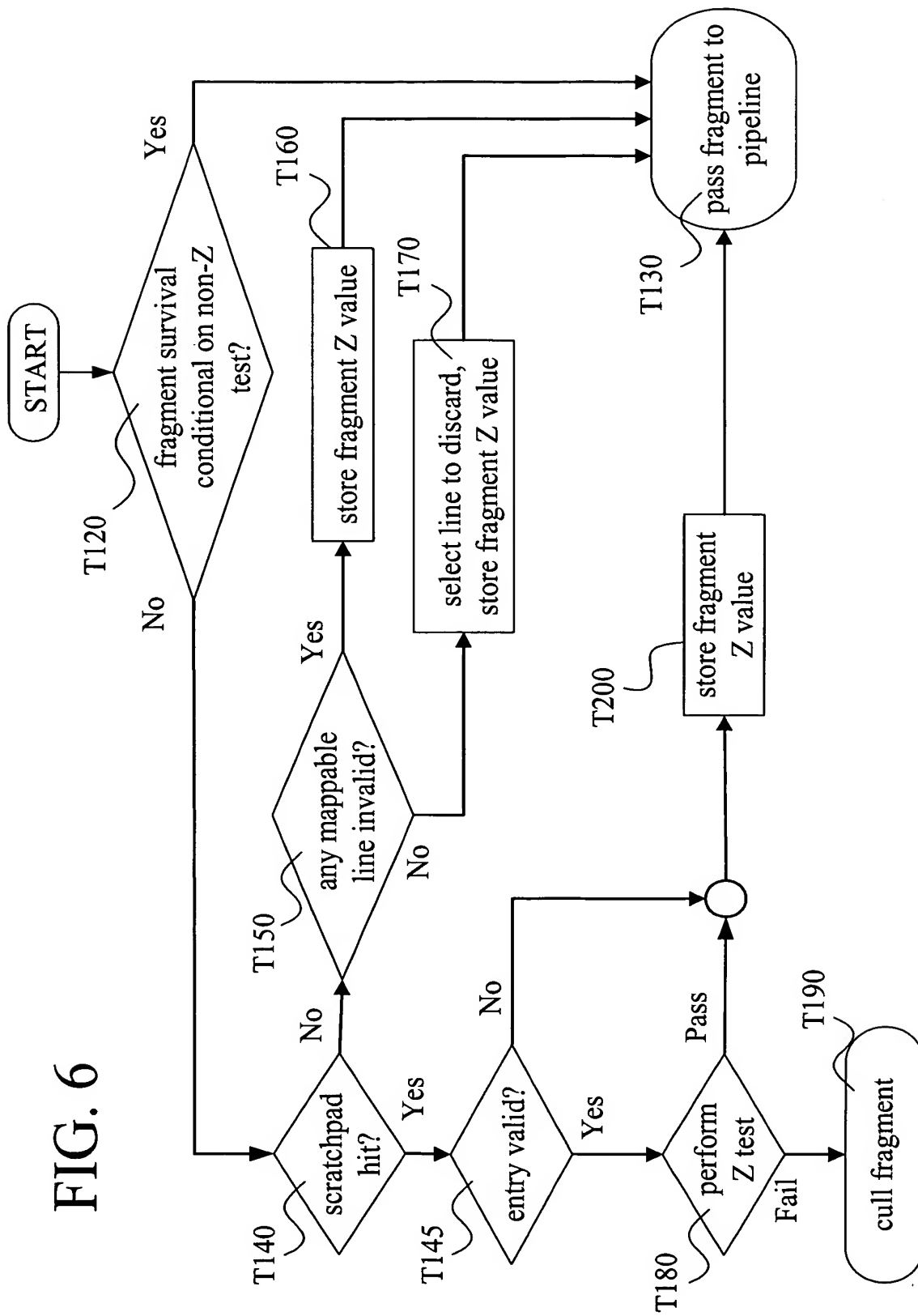
display  
frame 10

pixel  
block 20

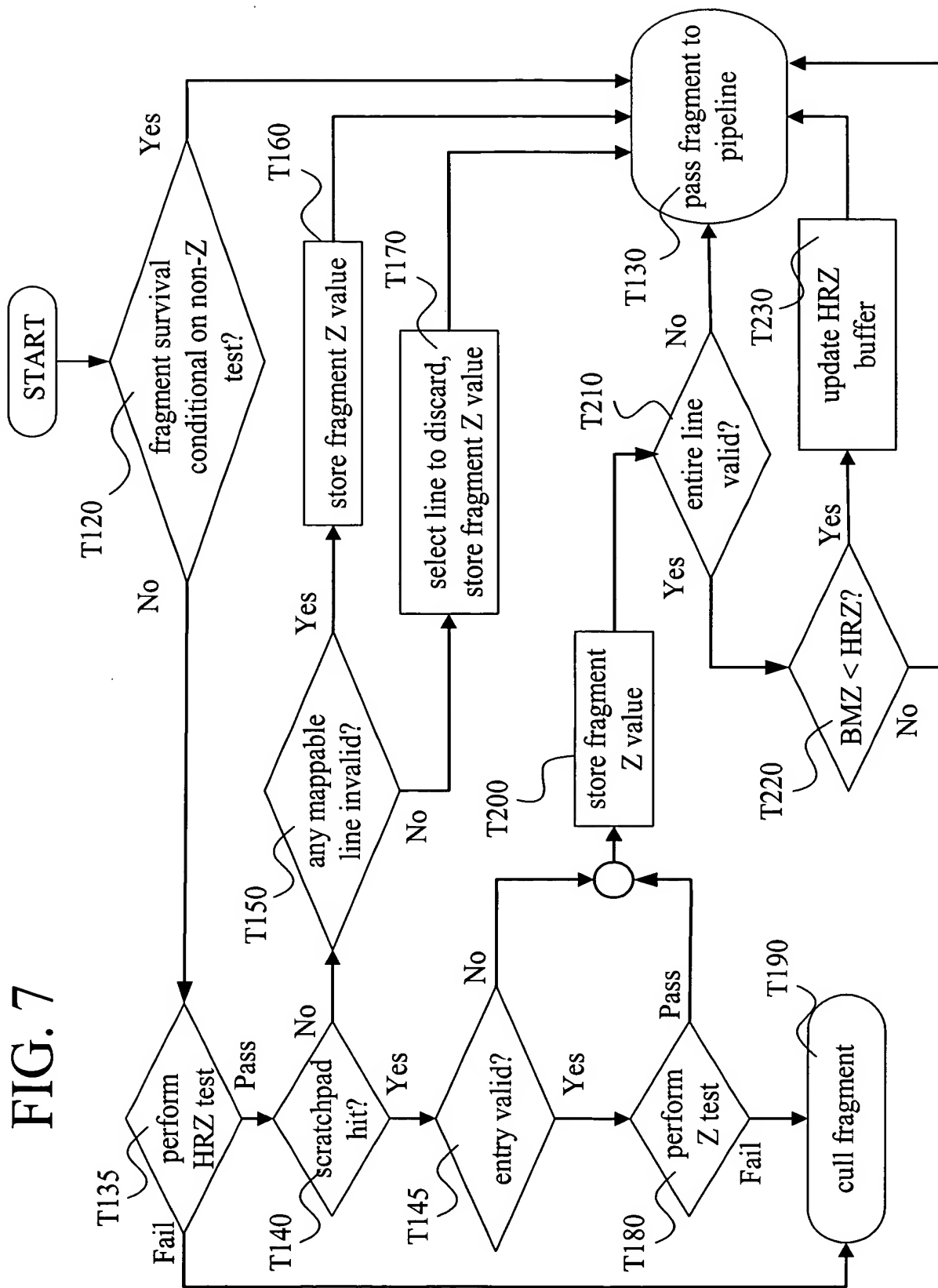
pixel 30

FIG. 5

FIG. 6



# FIG. 7



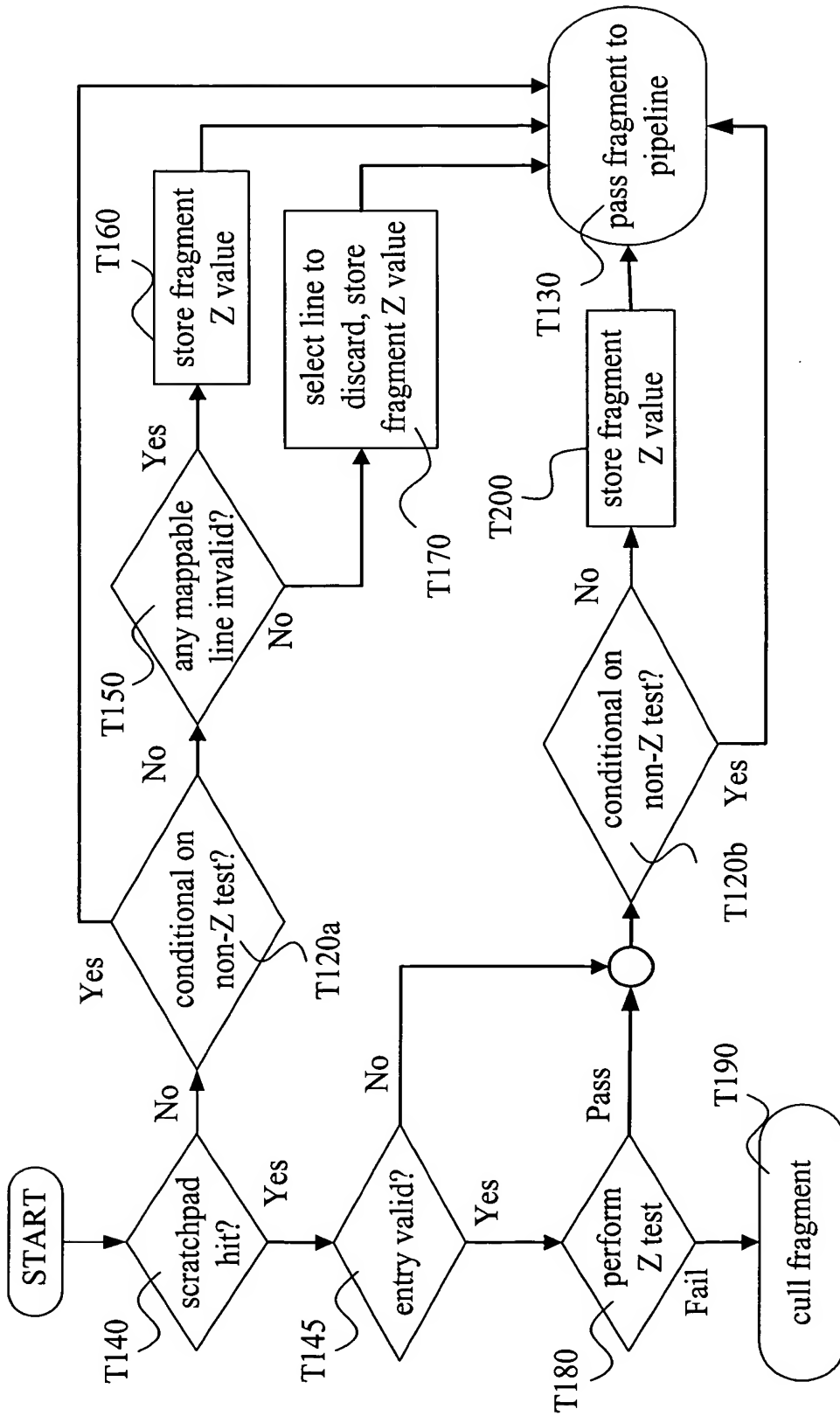


FIG. 8



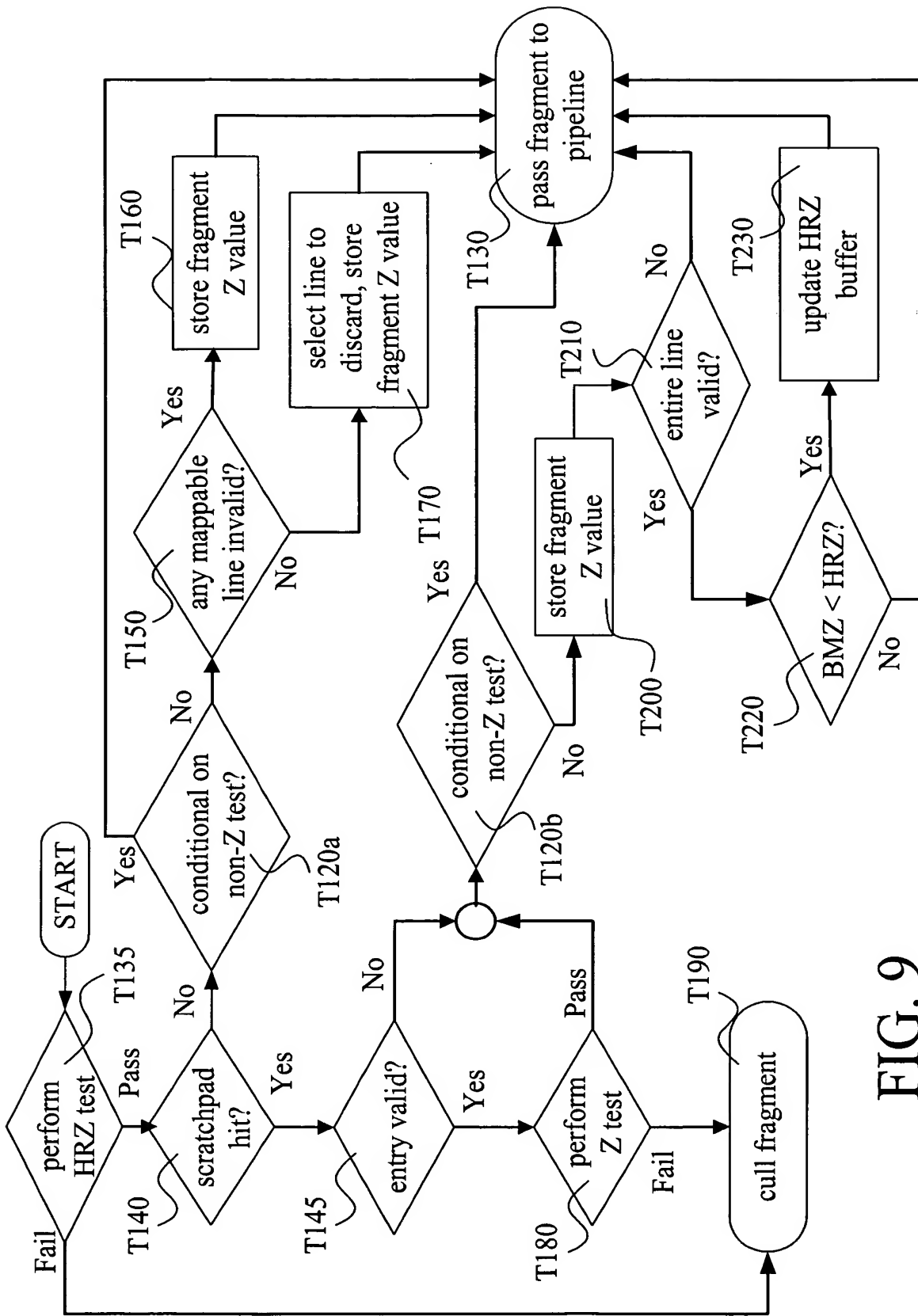


FIG. 9

FIG. 10

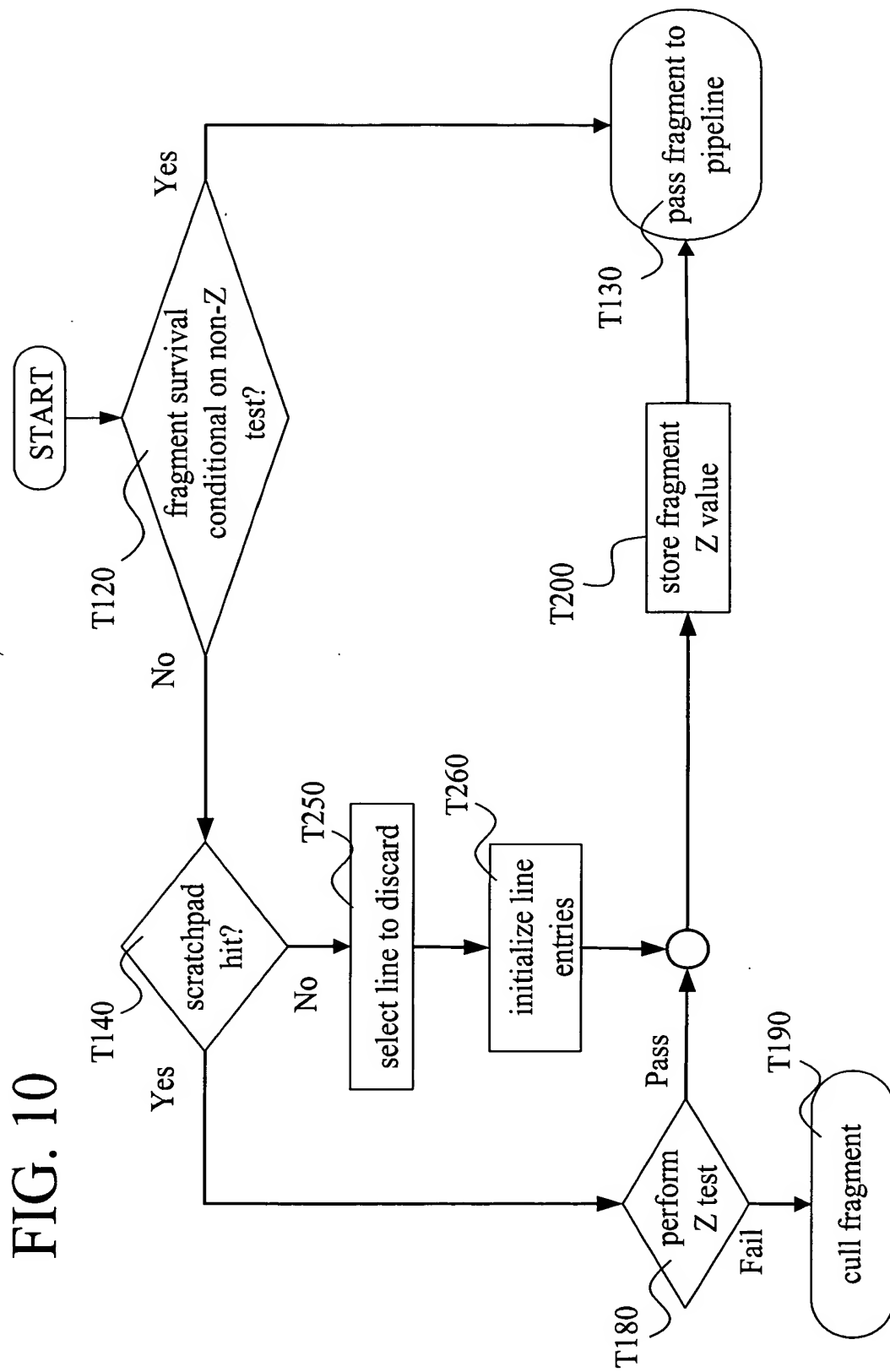


FIG. 11

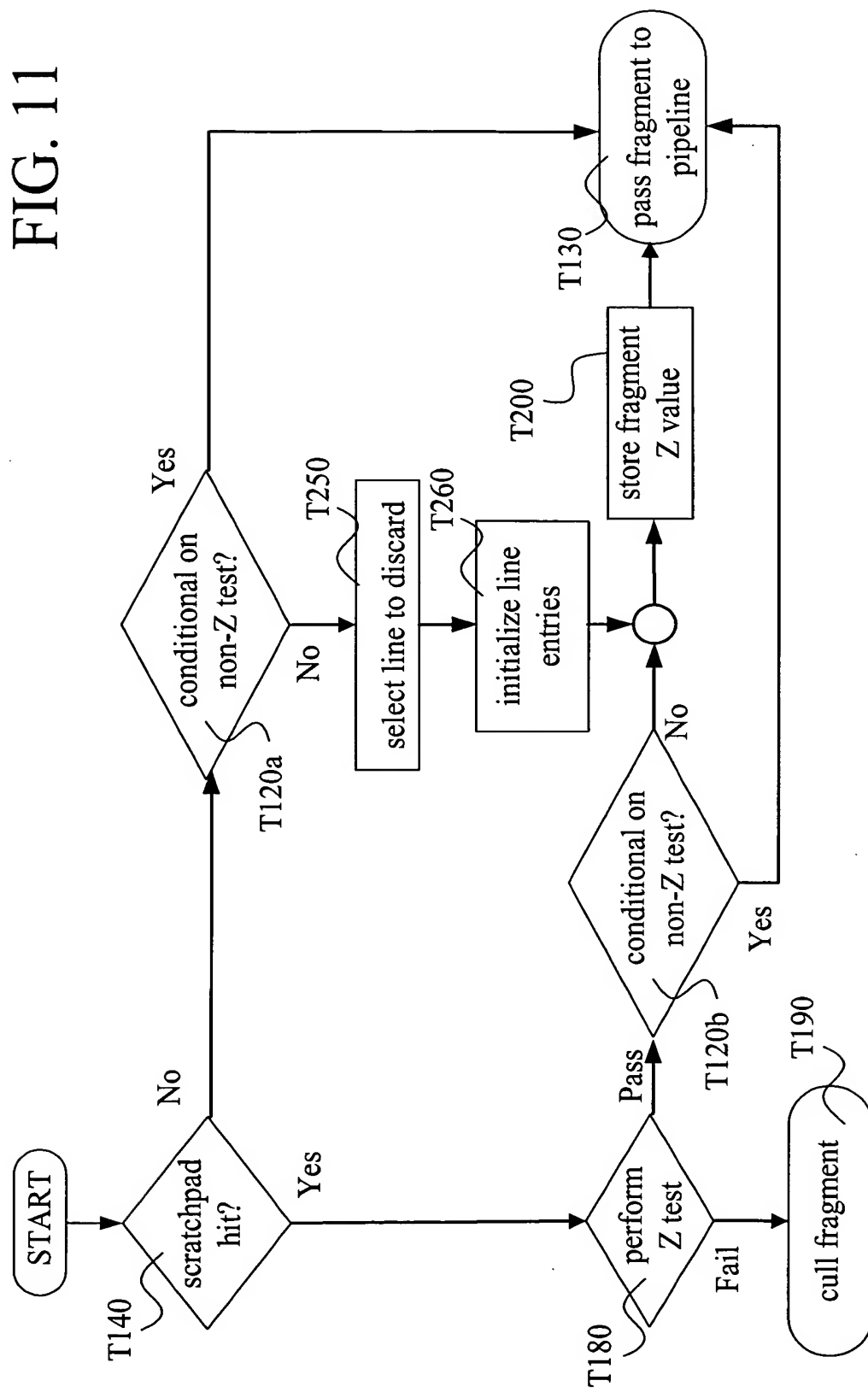


FIG. 12

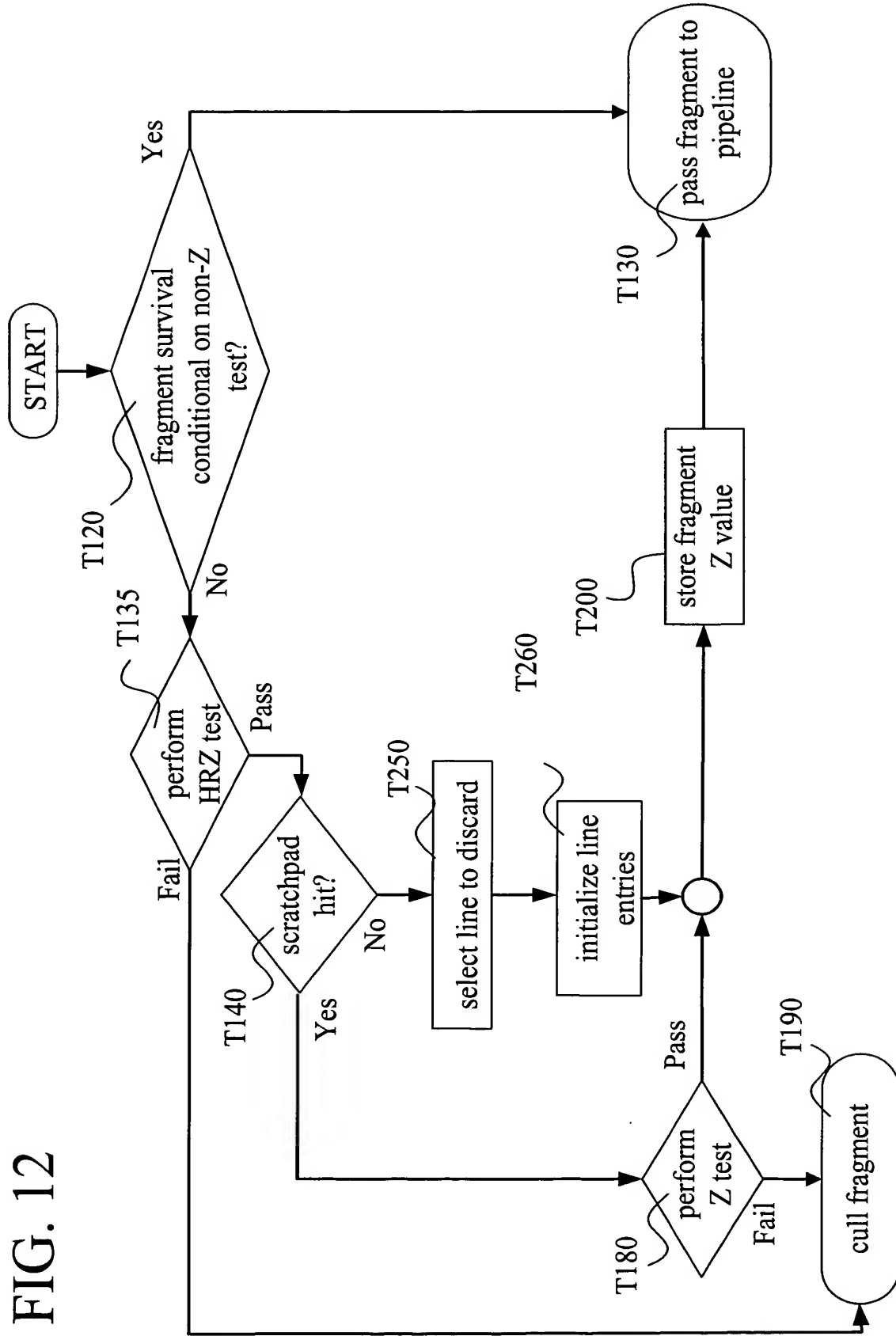


FIG. 13

